

Accelerating Large Language Model Use Cases with Intel Flex GPU and Intel Extension for PyTorch (IPEX)



OUTLINE

INTRODUCTION	2
Experiment Setup	3
Evaluation Results & Analysis	4
Conclusion	8
References	8

TABLES

TABLE 1: MODEL EXPERIMENT CONFIGURATIONS	3
TABLE 2: PERFORMANCE COMPARISON BETWEEN INTEL CPU AND INTEL FLEX GPU.	4

FIGURES

FIGURE 1: GPT-2 BENCHMARK RESULT	4
Figure 2: BERT benchmark result	5
Figure 3 : ResNet benchmark result	5
FIGURE 4: GPT-2 PROFILING RESULT	6
FIGURE 5: BERT PROFILING RESULT	7
Figure 6: ResNet profiling result	7

Introduction

Large Language Models (LLMs) are powerful tools for generating text in a wide range of applications. By training on vast datasets, LLMs can produce coherent, contextually appropriate responses based on input prompts. Their ability to understand and generate human-like text makes them suitable for various tasks, such as content creation, question answering, and text summarization.

LLMs are highly versatile, enabling use cases across industries. They can assist in automating writing processes, enhancing customer interactions, improving language translation, or even generating creative content like stories and poems. Their applications span multiple domains, from everyday text-based tasks to more complex interactions in virtual assistants or conversational agents. In industry settings, LLMs have become essential for automating and improving text-based workflows. For example:

- **Content Marketing**: LLMs generate high-quality blog posts, product descriptions, and social media content, reducing the need for manual content creation. By producing engaging and tailored content efficiently, businesses can scale their marketing efforts with minimal human intervention.
- **Customer Support**: LLM-powered chatbots and virtual assistants handle routine customer queries in real-time, providing accurate and helpful responses. This not only enhances customer satisfaction but also reduces the workload on human agents, allowing them to focus on more complex issues.
- Healthcare: LLMs assist in medical documentation, including writing patient reports, summarizing clinical notes, and generating discharge summaries. They ensure that healthcare professionals save time on administrative tasks, allowing them to focus more on patient care.
- Legal and Financial: LLMs assist in drafting legal documents or generating financial reports, saving time on manual tasks while ensuring high accuracy in complex fields.

To ensure optimal performance and cost-effectiveness in LLM deployment, it's crucial to evaluate their inference capabilities. To this end, we benchmarked LLM models on the QCT DevCloud platform¹, a collaborative effort between Quanta Cloud Technology (QCT) and Intel. This platform is specifically designed to address the unique challenges of developing and testing high-performance computing (HPC) and AI applications.

QCT DevCloud provides a comprehensive remote development environment, granting access to cutting-edge hardware, storage, and networking resources. Leveraging the advanced features of Intel Xeon processors, such as enhanced security and accelerated HPC/AI capabilities, this platform streamlines development workflows. Precompiled workloads, a robust software stack, and tools like Intel oneAPI further simplify the process by eliminating the need for complex installations.

Moreover, QCT DevCloud enables testing across various CPU and GPU environments, including Intel Flex 170 GPUs. This flexibility empowers developers to efficiently evaluate system performance, reducing development time and resource constraints, ultimately leading to significant cost savings. By utilizing this platform, industries can optimize their LLM deployments, increasing productivity, reducing labor costs, and streamlining operations.

Experiment Setup

To evaluate the performance of LLM inference, we benchmarked the models on an Intel Flex 170 GPU, utilizing Intel Extension for PyTorch (IPEX) to optimize deep learning workloads. IPEX enhances PyTorch operations by maximizing the use of Intel hardware features, such as the Flex GPU, improving overall speed and efficiency.

In this use case, we evaluate three models to demonstrate AI performance across various tasks:

- **GPT-2**²: A generative language model from OpenAI, ideal for text generation tasks like completion, summarization, and creative writing.
- **BERT**³: A transformer-based model designed for natural language understanding, excelling in tasks like text classification and question answering.
- **ResNet**⁴: A convolutional neural network widely used for image classification, known for its effectiveness in computer vision tasks.

The ResNet model is included to compare the performance of LLMs with the traditional image classification model. The experiment setup of these models is summarized in the **Table 1** below.

	Batch Size	Configurations	
GPT-2	64	Max Output Length: 128 tokens Iterations: 20	
BERT	256	Max Output Length: 512	
ResNet	512	Image Size: 224 × 224 pixels, 3 channels (RGB)	

Table 1: Model Experiment Configurations

All models were optimized using **Intel Extension for PyTorch (IPEX)** on Intel Flex GPUs to enhance performance and efficiency compared to traditional CPU-based processing.

Intel Extension for PyTorch (IPEX) is a performance optimization library designed to enhance PyTorch's deep learning workloads on Intel hardware. IPEX integrates seamlessly with PyTorch, allowing users to take advantage of Intel-specific optimizations without modifying their existing code.

By leveraging IPEX, deep learning models like GPT-2, BERT, and ResNet can experience significant speedups on Intel Flex GPUs, delivering improved efficiency across various AI applications.

Evaluation Results & Analysis

The performance comparison results between Intel Flex GPU and Intel Xeon 8470 CPU are shown in Figure 1 ~ Figure 3 for the three tested models, respectively. The overall speedup factor of Intel GPU over Intel CPU is summarized in Table 2. The results show a significant speedup in text generation and inference when running models on the Intel Flex GPU with IPEX optimizations compared to the CPU. For GPT-2, the Intel Flex GPU was more than 3 times faster than the CPU, demonstrating the efficiency gains in LLM text generation. For BERT, the GPU accelerated inference performance by almost 8 times, highlighting the Flex GPU's ability to handle high-volume sequence-based tasks. Finally, although the speedup for ResNet image classification was less pronounced, the GPU still outperformed the CPU by 1.6 times, showing how Intel Flex GPUs can enhance vision-related workloads as well. The result also indicates that the Intel Flex GPU can have better performance acceleration for the text-based models, like GPT-2 and BERT, than the vision models like ResNet.

	CPU (Intel Xeon 8470) Total Time (sec)	XPU (Intel Flex GPU) Total Time (sec)	Speedup Factor of Intel Flex GPU	
GPT-2	85.12	27.37	3.10	
BERT	5.557	0.697	7.97	
ResNet	1.711	1.064	1.60	

Table 2: Performance Comparison Between Intel CPU and Intel Flex GPU.



Benchmarking Results: Total Time for GPT-2 Text Generation

Figure 1: GPT-2 benchmark result.

Benchmarking Results: BERT - CPU vs XPU



Figure 2: BERT benchmark result.



Benchmarking Results: ResNet - CPU vs XPU

Figure 3 : ResNet benchmark result.

We also profiled the performance using **Intel VTune Profiler**, a tool designed to analyze and optimize code running on Intel hardware with the following command, "vtune -collect gpu-offload python {program name}.py".

The profiling results for the three tested models are presented in Figure 4 to Figure 6. These figures illustrate that the workload is compute-bound based on the following observations:

- **Execution Time Dominance**: Figure 4 demonstrates that GPU 0 achieves an execution duration of 977.748 milliseconds, with a predominant allocation of time to the execution phase as opposed to host-to-device or device-to-host data transfer operations. This delineation underscores the computationally intensive nature of the workload, with minimal overhead attributed to data movement.
- **GPU Utilization**: The green bars in the timeline represent GPU computation tasks, which are active for most of the execution period. The dense and continuous nature of these bars suggests that the GPU is fully utilized and not idling, reinforcing the conclusion that the workload is compute-bound.
- **Call Stack Analysis**: The call stack reveals that most of the operations (e.g., Abstract Tensor and Torch library functions) are computation focused. This further emphasizes the focus on core computation tasks rather than memory-bound or other bottlenecks.

Similar patterns are also observed in **Figure 5** and **Figure 6**. According to the profiling data, the workload is primarily constrained by the computational capacity of the hardware. As a result, Intel GPUs demonstrate significant performance acceleration compared to CPUs in these scenarios.



Figure 4: GPT-2 profiling result.

Welcome × r002go × r0	003qo × r004qo ×					- 0	×
GPU Offload GPU Off	load 🗸 🕐					INTEL VTUNE PRO	DFILER
Analysis Configuration Colle	ection Log Summary Graphics	Platform					
Grouping: GPU Adapter / GPU	Computing Task / Host Call Stack	- Iddonni	~	<u>×</u> Q & L	Call Stacks		n ≞
GPU Adapter / GPU Computing Task / Host Call Stack	Total Time by I Allocation Host-to-Device Tran	evice Operation Type ▼ sfer Execution Devi	ce-to-Host Transfer	Instance Count	Execution (GPU Computing Task	<) ~	
▶ GPU 0	675.901ms			6	30.7% (0.18	ubmit_impl+0x14	,
▶ GPU 1					libiptel ext at apu co svolv 1/1	abmit_impl+0x14	
▶ GPU 3					libintei-ext-pt-gpu.so i sych:_v1::	.queue::subinit_impi+0xzaa	1
▶ GPU 2					libintel-ext-pt-gpu.so dnnl::impl:	::gpu::syci::syci_interop_gp	
					libintel-ext-pt-gpu.so ! dnnl::impl:	::gpu::jit::gen_gemm_t::lau.	
					libintel-ext-pt-gpu.so ! dnnl::impl:	::gpu::jit::gen_gemm_t::exe	
					libintel-ext-pt-gpu.so ! dnnl::impl:	::gpu::ocl::gemm_matmul_t	
					libintel-ext-pt-gpu.so ! dnnl_primi	itive::execute+0x27b	
					libintel-ext-pt-gpu.so ! dnnl::impl:	:primitive_execute+0x6af	
					libintel-ext-pt-gpu.so ! dnnl_sycl_	_interop_primitive_execute.	
					libintel-ext-pt-gpu.so ! dnnl::sycl_	interop::execute+0x96c4	
					libintel-ext-pt-gpu.so ! xpu::oneD	NN::matmul+0xffb	
					libintel-ext-pt-gpu.so at::AtenIpe	exTypeXPU::addmm out+0)
					libtorch cpu.so ! c10::impl::wrap	kernel functor unboxed .	
					libtorch coulso at: ons.addm	m_out-call+0x70	
D: 🕇 — 🖛 🕯	🖍 Os 5s 10s	15s 20s	25s 30s	35s	40s 45s 50s	Thread	~
ad			11			📕 🗹 🔲 Running	
မိ python (TID: 1396905)						CPU Time	
						🗌 🗆 🛡 CPU Sample	
0 00U 2						User Tasks	
E GPU 3						GPU Comput	ting
GPU 2						GPU Vector Engi	ine
ਤੂੱ GPU 1						XVE Arrays	
듈 GPU 3						Active	
GPU 2							
କ୍ର GPU 1							Thr
	ra1	Thursday The second			0-11-01		
TILIER 100.0%	Process Any Process V	Any Inread		Judie Any Module	e V Can Sta	ack mode user functions +	· 1 • •

Figure 5: BERT profiling result.

- 0 ×

Welcome × r002go × r0	03go × r004go ×					
GPU Offload GPU Off	oad 🕶 🕐				INTEL V	TUNE PROFILER
Analysis Configuration Colle	ction Log Summary	Graphics Platform				
Grouping: GPU Adapter / GPU	Computing Task / Host	Call Stack		✓ ≪ Q Q 20 ≥	Call Stacks	∿ ∄
GPU Adapter / GPU Computing Task / Host Call Stack	T Allocation Host-to	otal Time by Device Operatio	on Type 🔻 tion 📲 Device-to-Host Tr	ansfer Instance Cour	Execution (GPU Computing Task) ~	
▶ GPU 0	647.593ms		-	7	3 libeval so 7 sval: V1::gueue:submit_impl=0x14	/
▶ GPU 1					libistel ext at any ee Levely Magueueueubmit im	apl. 0x2aa
▶ GPU 3					libintel-ext-pt-gpu.so syciv1queuesubmit_in	
▶ GPU 2					libintei-ext-pt-gpu.so ! dnni::impl::gpu::syci::syci_in	nerop_gp
					libintel-ext-pt-gpu.so anni::impl::gpu::jit::gen_con	volution_t
					libintel-ext-pt-gpu.so ! dnnl_primitive::execute+0x2	27b
					libintel-ext-pt-gpu.so ! dnnl::impl::primitive_execute	e+0x6af
					libintel-ext-pt-gpu.so ! dnnl_sycl_interop_primitive_	_execute
					libintel-ext-pt-gpu.so dnnl::sycl_interop::execute+	+0x239f
					libintel-ext-pt-gpu.so ! xpu::oneDNN::convolution+	0x25a1
					libintel-ext-pt-gpu.so ! at::AtenIpexTypeXPU::_con	volution
					libintel-ext-pt-gpu.so at::AtenIpexTypeXPU::_con	volution+
					libintel-ext-pt-gpu.so at::AtenIpexTypeXPU::conv	olution_o
					libtorch_cpu.so ! c10::impl::wrap_kernel_functor_u	unboxed
)			libtorch_cnu.so.Lat::_ons::convolution_overrideab	lecall+0
D: 🕇 = 🖻 🖉	🖻 Os 5s	10s 1	L5s 20s	25s 3	0s 35s 40s 🗹 Thread	v b
python (TID: 1399316)	Π					unning PU Time PU Sample
Puthon (TID: 1400035)					Us	ser Tasks
GPU 3	_				GF	PU Computing
						ector Engine
					AVE Alla	Iys Active
D GPU 3						Idle
GPU 2	_					Stalled
g GPU 1					GPU C	omputing Thr
FILTER 100.0%	Process Any Proc	ess	Thread	V Module Any Modu	le V Call Stack Mode User fr	unctions + 1 ×

Figure 6: ResNet profiling result.

Conclusion

This experiment demonstrates the efficiency of using Intel Flex 170 GPUs in conjunction with Intel Extension for PyTorch (IPEX) for several tasks. The benchmarks show that GPU acceleration, combined with IPEX optimizations, drastically reduces the time required for generating outputs. With the Intel Flex GPU completing the task nearly three times faster than the CPU, this makes it an ideal solution for businesses and organizations looking to deploy AI applications.

By leveraging the power of **Intel Flex GPUs** and the efficiency enhancements provided by **IPEX**, industries can significantly optimize their AI workflows. The improved speed and resource efficiency can lead to reduced operational costs, faster response times, and enhanced user experience. The combination of Intel's hardware and software optimizations positions it as a highly scalable option for AI-driven applications. Additionally, the QCT DevCloud platform has streamlined the setup and preparation process for the system and development environment. The platform also enables efficient evaluation of resource requirements, helping us identify optimal configurations to support high-performance computing needs.

References

- 1. Ware A. QCT and Intel Codeveloped QCT DevCloud Program to Jumpstart HPC and AI Development. HPCwire. August 7, 2023. Accessed December 4, 2024. https://www.hpcwire.com/2023/08/07/qct-devcloud-program-provides-a-platform-to-jumpstart-hpc-and-ai-development/
- 2. Radford A, Wu J, Child R, Luan D, Amodei D, Sutskever I. Language models are unsupervised multitask learners. *OpenAI Blog*. 2019;1(8):9.
- 3. Devlin J, Chang MW, Lee K, Toutanova K. BERT: Pre-training of Deep Bidirectional Transformers for Language Understanding. Published online May 24, 2019. doi:10.48550/arXiv.1810.04805
- 4. He K, Zhang X, Ren S, Sun J. Deep Residual Learning for Image Recognition. Published online December 10, 2015. doi:10.48550/arXiv.1512.03385